|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Pino TRDB-D5M / Pino GPIO\_1 (DE2\_70) | Signal Name (TRDB-D5M) | Signal Name (DE2\_70) | FPGA PIN Nº | VHDL Port |
| 1 | PIXCLK | IO\_CLKINn1 | PIN AH14 | IO\_CLKINn1 |
| 2 | D[11] | IO\_B[0] | PIN G27 | GPIO\_1[0] |
| 3 | NC | IO\_CLKINp1 | PIN AG15 | IO\_CLKINp1 |
| 4 | D[10] | IO\_B[1] | PIN G28 | GPIO\_1[1] |
| 5 | D[9] | IO\_B[2] | PIN H27 | GPIO\_1[2] |
| 6 | D[8] | IO\_B[3] | PIN L24 | GPIO\_1[3] |
| 7 | D[7] | IO\_B[4] | PIN H28 | GPIO\_1[4] |
| 8 | D[6] | IO\_B[5] | PIN L25 | GPIO\_1[5] |
| 9 | D[5] | IO\_B[6] | PIN K27 | GPIO\_1[6] |
| 10 | D[4] | IO\_B[7] | PIN L28 | GPIO\_1[7] |
| 11 | NC | VCC5 | --- | --- |
| 12 | GND | GND | --- | --- |
| 13 | D[3] | IO\_B[8] | PIN K28 | GPIO\_1[8] |
| 14 | D[2] | IO\_B[9] | PIN L27 | GPIO\_1[9] |
| 15 | D[1] | IO\_B[10] | PIN K29 | GPIO\_1[10] |
| 16 | D[0] | IO\_B[11] | PIN M25 | GPIO\_1[11] |
| 17 | NC | IO\_B[12] | PIN K30 | GPIO\_1[12] |
| 18 | NC | IO\_B[13] | PIN M24 | GPIO\_1[13] |
| 19 | XCLKIN | IO\_CLKOUTn1 | PIN AF27 | IO\_CLKOUTn1 |
| 20 | RESETn | IO\_B[14] | PIN L29 | GPIO\_1[14] |
| 21 | NC | IO\_CLKOUTp1 | PIN AF28 | IO\_CLKOUTp1 |
| 22 | TRIGGER | IO\_B[15] | PIN L30 | GPIO\_1[15] |
| 23 | STROBE | IO\_B[16] | PIN P26 | GPIO\_1[16] |
| 24 | LVAL | IO\_B[17] | PIN P28 | GPIO\_1[17] |
| 25 | FVAL | IO\_B[18] | PIN P25 | GPIO\_1[18] |
| 26 | SDATA | IO\_B[19] | PIN P27 | GPIO\_1[19] |
| 27 | SCLK | IO\_B[20] | PIN M29 | GPIO\_1[20] |
| 28 | NC | IO\_B[21] | PIN R26 | GPIO\_1[21] |
| 29 | VCC33 | VCC33 | --- | --- |
| 30 | GND | GND | --- | --- |
| 31 | NC | IO\_B[22] | PIN M30 | GPIO\_1[22] |
| 32 | NC | IO\_B[23] | PIN R27 | GPIO\_1[23] |